


2002P15288US
Taucher et al.**Certificate of Facsimile Transmission**

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In re Application of: Herbert TAUCHER et al.

Art Unit: 2133

Serial No.: 10/670,567

Examiner: Binh C. TAT

OCT 12 2005

Filing Date: 09/26/2003

Atty. Docket: 2002P15288US

For: *Method and device for testing the
mapping/implementation of a model of a
logic circuit onto/in a hardware emulator*

Confirmation No.: 2825

RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents
United States Patent & Trademark Office
Customer Service Window, Mail Stop AMENDMENT
Randolph Building
401 Dulany Street
Alexandria, VA 22314 USA

INTRODUCTORY COMMENTS

Sir:

In response to the Office Action dated July 12, 2005, Applicants submit the following amendment and remarks. Appropriate consideration by the Patent Office is respectfully requested.

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